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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of	3
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Complete if Known

Application Number	10/058,258
Filing Date	January 25, 2001
First Named Inventor	Julie WARD
Art Unit	2154
Examiner Name	Philip C. LEE
Attorney Docket Number	10019700-1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{3 (if known)}	MM-DD-YYYY		
WTL	1A	US- 2002-0091804-A1	07-11-2002	Ward et al.	
↑	1B	US- 2002-0091845-A1	07-11-2002	Ward et al.	
	1C	US- 2002-0120770-A1	08-29-2002	Parham et al.	
	1D	US- 2002-0156828-A1	10-24-2002	Ishizaki et al.	
	1E	US- 2002-0188732-A1	12-12-2002	Buckman et al.	
	1F	US- 2003-0144822-A1	07-31-2003	Peh et al.	
	1G	US- 2005-0021831-A1	01-27-2005	Andrzejak et al.	
	1H	US- 2005-0033844-A1	02-10-2005	Andrzejak et al.	
	1I	US- 5,426,674-A	01-20-1995	Nemirovsky et al.	
	1J	US- 5,598,532-A	01-28-1997	Liron	
	1K	US- 5,649,105-A	07-15-1997	Aldred et al.	
	1L	US- 5,793,362-A	08-11-1998	Matthews et al.	
	1M	US- 5,987,517-A	11-16-1999	Firth et al.	
	1N	US- 6,052,360-A	04-18-2000	Rogers	
	1O	US- 6,345,048-B1	02-05-2002	Allen et al.	
	1P	US- 6,418,481-B1	07-09-2002	Mancusi et al.	
	1Q	US- 6,442,584-B1	08-27-2002	Kolli et al.	
✓	1R	US- 6,452,924-B1	09-17-2002	Golden et al.	
WTL	1S	US- 6,539,027-B1	03-25-2003	Cambion	

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Wen Jan Lin

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PTO/SB/08A (08-03)

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antl ↑	2A	US- 6,598,080-B1	07-22-2003	Nagami et al.	
	2B	US- 6,625,777-B1	09-23-2003	Levin et al.	
	2C	US- 6,633,909-B1	10-14-2003	Barrett et al.	
	2D	US- 6,650,639-B2	11-18-2003	Doherty et al.	
	2E	US- 6,675,328-B1	01-06-2004	Krishnamachari et al.	
	2F	US- 6,694,361-B1	02-17-2004	Shah et al.	
	2G	US- 6,697,334-B1	02-24-2004	Klincewicz et al.	
	2H	US- 6,697,369-B1	02-24-2004	Dziong et al.	
	2I	US- 6,697,854-B1	02-24-2004	Glassen et al.	
	2J	US- 6,724,757-B1	04-20-2004	Zadikian et al.	
	2K	US- 6,744,767-B1	06-01-2004	Chiu et al.	
	2L	US- 6,778,496-B1	08-17-2004	Meempat et al.	
	2M	US- 6,804,245-B2	10-12-2004	Mitchem et al.	
	2N	US- 6,857,027-B1	02-15-2005	Lindeborg et al.	
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FOREIGN PATENT DOCUMENTS

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		Examiner Name	Philip C. LEE		
Sheet	3	of	3	Attorney Docket Number	10019700-1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
cutl	3A	Ravindra K. AHUJA et al., Network Flows: Theory, Algorithms, and Applications, pp. 4-9, 649-686, 1993, Prentice-Hall, Upper Saddle River, NJ.	
cutl	3B	Julie Ward DREW et al., US Patent Application No. 10/290,760, Filing Date: November 8, 2002.	
cutl	3C	Julie Ward DREW, US Patent Application No. 10/290,643, Filing Date: November 8, 2002.	
cutl	3D	Julie Ward DREW et al., US Patent Application No. 10/845,855, Filing Date: May 13, 2004.	

Examiner Signature	<i>Allen Jan L.</i>	Date Considered	3/9/06
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/08A (08-03)

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Sheet 1 of 6

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
CML	1A	US- 5,634,011-A	05-27-1997	Auerbach et al.	
	1B	US- 5,634,004-A	05-27-1997	Gopinath et al.	
	1C	US- 5,107,489-A	04-21-1992	Brown et al.	
	1D	US- 5,970,232-A	10-19-1999	Passint et al.	
	1E	US- 6,628,649-B1	09-30-2003	Raj et al.	
	1F	US- 6,614,796-B1	09-02-2003	Black et al.	
	1G	US- 5,831,996-A	11-03-1998	Abramovici et al.	
	1H	US- 5,878,232-A	03-02-1999	Peram Marimuthu	
	1I	US- 6,195,355-B1	02-27-2001	Noritoshi Demizu	
	1J	US- 6,701,327-B1	03-02-2004	Jones et al.	
	1K	US- 5,835,498-A	11-10-1998	Kim et al.	
	1L	US- 4,920,487-A	04-24-1990	Paul T. Baffes	
	1M	US- 5,329,619-A	07-12-1994	Pagé et al.	
	1N	US- 5,113,496-A	05-12-1992	McCalley et al.	
	1O	US- 6,594,701-B1	07-15-2003	Alessandro Forin	
	1P	US- 6,557,169-B1	04-29-2003	Bernard Erpeldinger	
	1Q	US- 6,766,381-B1	07-20-2004	Barker et al.	
CML	1R	US- 6,253,339-B1	06-26-2001	Tse et al.	
	1S	US- 6,701,327-B1	03-02-2004	Jones et al.	

FOREIGN PATENT DOCUMENTS

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		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				
CML	1T	WO-96/17458-A	06-06-1996	British Telecommunications Public Limited Company		

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Art Unit	2154
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Sheet 2 of 6**U. S. PATENT DOCUMENTS**

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		Number-Kind Code ² (if known)			
cut	2A	US- 5,524,212-A	06-04-1996	Somani et al.	
	2B	US- 6,603,769-B1	08-05-2003	Thubert et al.	
	2C	US- 6,757,731-B1	06-29-2004	Barnes et al.	
	2D	US- 6,148,000-A	11-14-2000	Feldman et al.	
	2E	US- 6,611,872-B1	08-26-2003	Steven McCanne	
	2F	US- 5,857,180-A	01-05-1999	Hallmark et al.	
	2G	US- 5,307,449-A	04-26-1994	Keiley et al.	
	2H	US- 5,651,005-A	07-22-1997	Kwok et al.	
	2I	US- 6,141,355-A	10-31-2000	Palmer et al.	
	2J	US- 5,838,919-A	11-17-1998	Schwaller et al.	
	2K	US- 6,038,219-A	03-14-2000	Mawhinney et al.	
	2L	US- 6,668,308-B2	12-23-2003	Barroso et al.	
	2M	US- 6,570,850-B1	05-27-2003	Gutierrez et al.	
	2N	US- 6,363,334-B1	03-26-2002	Andrews et al.	
	2O	US- 6,108,782-A	08-22-2000	Fletcher et al.	
	2P	US- 6,539,531-B2	03-25-2003	Miller et al.	
	2Q	US- 5,138,657-A	08-11-1992	Colton et al.	
	2R	US- 6,331,905-B1	12-18-2001	Ellinas et al.	
cut	2S	US- 6,003,037-A	12-14-1999	Kassabgi et al.	

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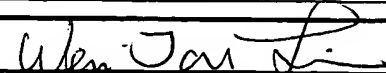
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Cute	3A	US- 5,245,609-A	09-14-1993	Ofek et al.	
	3B	US- 5,805,578-A	09-08-1993	Stirpe et al.	
	3C	US- 6,047,199-A	04-04-2000	DeMarco	
	3D	US- 5,581,689-A	12-03-1996	Slominski et al.	
	3E	US- 5,815,402-A	09-29-1998	Taylor et al.	
	3F	US- 6,157,645-A	12-05-2000	Shobatake	
	3G	US- 6,212,568-B1	04-03-2001	Miller et al.	
	3H	US- 2003-0145294 A1	07-31-2003	Ward et al.	
	3I	US- 2003-0065758 A1	04-03-2003	O'Sullivan et al.	
	3J	US- 2002-0083159 A1	06-27-2002	Ward et al.	
	3K	US- 6,031,984-A	02-29-2000	Walser	
	3L	US- 6,526,420-B2	02-25-2003	Borowsky et al.	
	3M	US- 2005-0021583-A1	01-17-2005	Andrzejak et al.	
Cute	3N	US- 6,687,222-B1	02-03-2004	Albert et al.	
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Sheet	4	of	6	Attorney Docket Number	10019700-1

NON PATENT LITERATURE DOCUMENTS			
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Cute	4A	Mathew ANDREWS et al., Integrated Scheduling of Unicast and Multicast Traffic in an Input-Queued Switch, 1999. < http://cm.bell-labs.com/cm/ms/who/andrews/infocom99switch.ps >	
↑	4B	Viraphol CHAIYAKUL, Assignment Decision Diagram for High-Level Synthesis, 1992.	
	4C	Andre DEHON, Notes on Coupling Processors with Reconfigurable Logic, MIT Transit Project, Transit Note #118, 1995.	
	4D	Cathy FULTON et al., Impact Analysis of Packet-Level Scheduling On an ATM Shared-Memory Switch, Infocom, Volume 3 pp. 947-954, 1998, IEEE, New York, NY.	
	4E	Hiroshi INOSE, An Introduction to Digital Integrated Communication Systems, pages 87-89, 1979, Peter Peregrinus Ltd., Stevenage, United Kingdom.	
	4F	A. Richard NEWTON, Interface-Based Design; Introduction, University of California at Berkeley, April 1999.	
	4G	Derek C. W. PAO, A Congestion Control Algorithm for Multipoint-to-Multipoint ABR Service in ATM Network, Proceedings of the IEEE Conference on High Performance Switching and Routing, pp. 167-175, June 26, 2000, IEEE Press, New York, NY.	
	4H	Rainer SCHOENEN et al., Weighted Arbitration Algorithms with Priorities for Input-Queued Switches with 100% Throughput, 1999, < http://www.iss.rwth-aachen.de/Projekte/Theo/papers/Schoenen99bssw.ps.gz >	
↓	4I	Oryal TANIR et al., Structural Reuse In The Design Of ATM Switch Fabrics, 1997.	
Cute	4J	I. WIDJAJA et al., Performance Issues in VC-Merge Capable ATM LSRs, RFC 2682, September 1999, The Internet Society, Reston, VA.	

Examiner Signature	<i>William J. L.</i>	Date Considered	3/9/06
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known			
		Application Number	10/058,258		
		Filing Date	January 25, 2002		
		First Named Inventor	Julie WARD		
		Art Unit	2154		
		Examiner Name	Philip C. LEE		
Sheet	5	of	6	Attorney Docket Number	10019700-1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
cutl	5A	Julie WARD, US Patent Application No.: 09/707,227, Filing Date: November 6, 2000.	
↑	5B	D. KLINGMAN et al. Netgen: A Program for Generating Large Scale Capacitated Assignment, Transportation, and Minimum Cost Flow Network Problems, Management Science 20(5):814-821, 1974, The Institute Of Management Sciences, Providence, RI.	
	5C	Dimitri P. BERTSEKAS, Linear Network Optimization: Algorithms and Codes, pages 253-260, 1991, The MIT Press, Cambridge, MA.	
	5D	Julie WARD et al., Appia: Automatic Storage Area Network Fabric Design, Conference on File and Storage Technologies (FAST'02), pp. 203-217, January 28, 2002, Usenix, Berkeley, CA.	
	5E	Staffan Bo STRAND, Storage Area Networks and SANTK, Thesis, December 2001, University of Minnesota, Minneapolis, MN.	
	5F	Matthew T. O'KEEFE et al., Designing Fibre Channel Storage Area Networks, February 2001. < http://www.borg.umn.edu/fc/papers/SANTK.pdf >	
	5G	Robert E. BIXBY, Solving Real-World Linear Programs: A Decade and More of Progress, January 2002.	
	5H	Ravindra K. AHUJA et al., Network Flows: Theory, Algorithms, and Applications, pp. 4-9, 649-686, 1993, Prentice-Hall, Upper Saddle River, NJ.	
↓	5I	Christodoulos A. FLOUDAS et al., Quadratic Optimization, 1995.	
cutl	5J	Parameswaran RAMANATHAN et al., Resource Placement with Multiple Adjacency Constraints in k-ary n-Cubes, 1995.	

Examiner Signature	<i>Allen Tai L.</i>	Date Considered	3/9/06
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cutl	6A	Staffan STRAND, Automatic Generation of Core/Edge Topology SANs Using SANTK, May 23, 2002.	
↑	6B	Joachim P. WALSER, Solving Linear Pseudo-Boolean Constraint Problems with Local Search, 1997, American Association for Artificial Intelligence, Menlo Park, CA.	
↓	6C	Julie WARD et al., Storage Area Network Fabric Design, PowerPoint presentation given at the Math Sciences Research Institute for Combinatorial Design, Berkeley CA, November 8, 2000.	
✓	6D	Julie WARD et al., Storage Area Network (SAN) Fabric Design, PowerPoint presentation given at the INFORMS International Conference, Maui HI, June 19, 2001.	
cutl	6E	Julie WARD et al., Storage Area Network (SAN) Fabric Design, PowerPoint presentation given at Berkeley, IEOR Department Seminar, Berkeley CA, October 8, 2001.	

Examiner Signature	<i>Allen Jan L.</i>	Date Considered	3/9/06
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